

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10050233			
<b>Filing Date:</b>	16-Jan-2002			
<b>Title of Invention:</b>	DELAY LOCKED LOOP SYNTHESIZER WITH MULTIPLE OUTPUTS AND DIGITAL MODULATION			
<b>First Named Inventor:</b>	Robert E. Stengel			
<b>Filer:</b>	Barbara R. Doutre/Vernice Freebourne			
<b>Attorney Docket Number:</b>	CM03359J			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
Utility Appt issue fee	1501	1	1400	1400
Publication fee for republication	1505	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1700